

FIG. 1

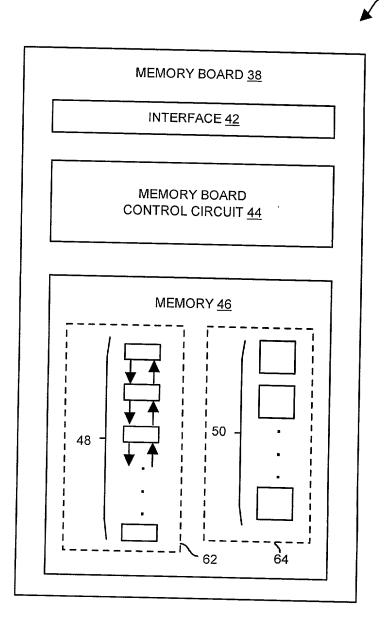


FIG. 2

FIG. 3

FIG. 4

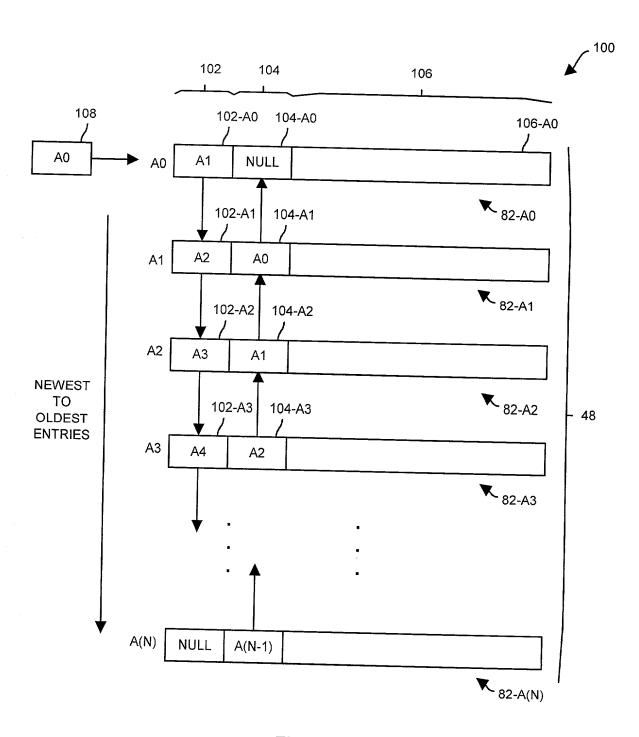


FIG. 5

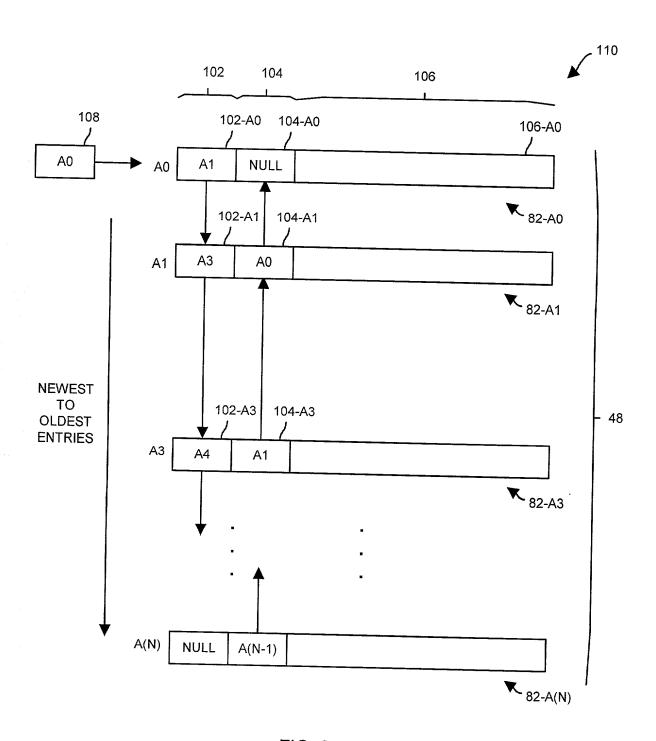


FIG. 6

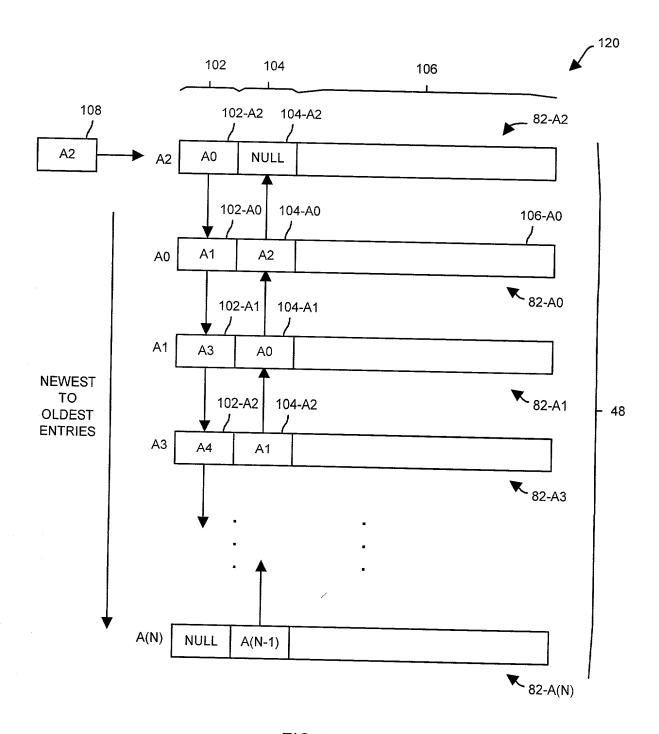


FIG. 7

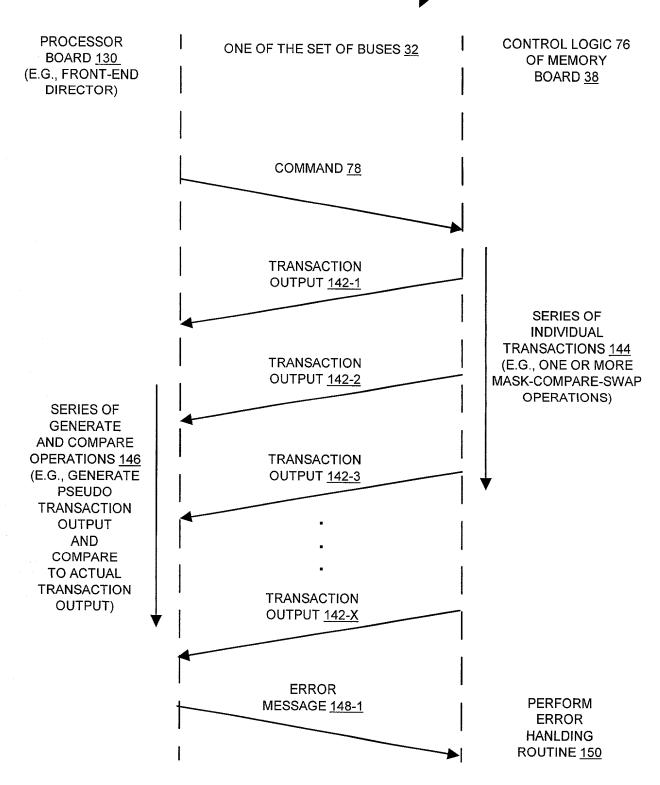
PROCESSOR BOARD <u>130</u> (E.G., FRONT-END CIRCUIT BOARD <u>36</u> OR BACK-END CIRCUIT BOARD <u>40</u>)

DATA PATHWAY/PIPE 132

CONTROLLER 134

DOUBLE-CHECKING CIRCUITRY <u>136</u>





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Santa Mana

FIG. 9